

Revision history		
Rev	Description	Date
A	Initial Release	12/19/2014
B	<p>ADC :</p> <ul style="list-style-type: none"> <li>- VDDOTP_SNS moved from ADC_SE11 to ADC0_SE9 channel.</li> <li>- USB_SNS moved from ADC_SE13 to ADC0_SE8 channel.</li> </ul> <p>=&gt; fix ADC conflicts</p> <p>CONTROL SIGNALS FOR BOOST REGULATOR :</p> <ul style="list-style-type: none"> <li>- BSTEN moved from PTB2 to PTIA2</li> <li>- BSTUP moved from PTB1 to PTIA4</li> <li>- BSTDN moved from PTB0 to PTIA5</li> </ul> <p>=&gt; free ADC pins for VDDOTP_SNS and USB_SNS</p> <p>I2C :</p> <ul style="list-style-type: none"> <li>- SDA/SCL moved from I2C1 channel to I2C0 channel.</li> </ul> <p>=&gt; fix I2C conflict between the two PF3000</p> <p>=&gt; no changes on the EVB</p> <p>Other changes:</p> <ul style="list-style-type: none"> <li>- PMIC_INTB now connected to FRDM-KL25Z board (PTAI1)</li> <li>- Removed 3.3V supply from FRDM-KL25Z board</li> <li>- Added a 3.3V LDO controled by PTC6</li> <li>- Output of the 3.3V LDO is connected to the ADC input : ADC0_SE14</li> <li>- Red LED control moved from PTC0 (GPIO2) to PTC7 (GPIO1)</li> <li>- Added two GND SMT Testpoints</li> <li>- Changed C9 to ceramic cap</li> </ul>	02/03/2015

