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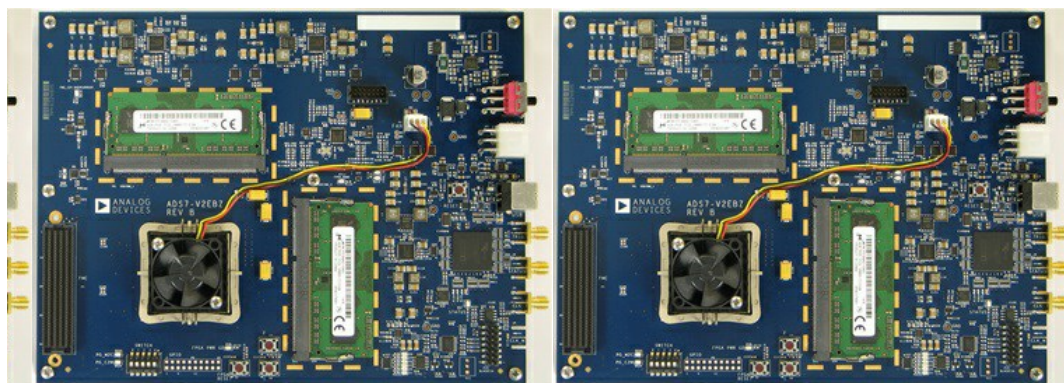
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ADS7-V2EBZ
FPGA Based Data Capture Kit

Overview

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Features

- Based on Virtex-7 FPGA
- One (1) FMC-HPC connector
- Ten (10) 13.1 Gbps transceivers supported
- Two (2) DDR3-1866 DIMMs
- Simple USB port interface (2.0)

Product Details

The ADS7-V2 Evaluation Board was developed to support the evaluation of Analog Devices high speed A/D converters, D/A converters and Transceivers with JESD204B bit rates up to 13.1 Gbps. The Quick Start Wiki site listed below provides a high level overview of the platform. In addition, each use case of the board has its own section (e.g. Using the ADS7-V2 for High Speed A/D Converter Evaluation). The ADS7-V2 is intended to be used only with specified Analog Devices Evaluation Boards. The ADS7-V2 is not intended to be used as a development platform, and no support is available for standalone operation. Please refer to Xilinx and its approved distributors for FPGA Development Kits

Documentation   ∧

        ① See All   ① Evaluation Documentation

Related Hardware ^

COMPATIBLE PRODUCT EVALUATION BOARDS

The High Speed Data Capture controller boards were designed to be used in conjunction with various ADI component evaluation boards as part of a customer evaluation environment. The following evaluation boards are compatible with High Speed Data Capture controller boards.

| High Speed ADC › 10MSPS (3) | ⌄ |
| IF/RF Receivers (1) | ⌄ |

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Evaluation Boards
Pricing displayed is based on 1-piece.

Model	Description	Price	RoHS
ADS7-V2EBZ Production	Evaluation Board		Yes

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Pricing displayed is based on 1-piece. The

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4,700+	125,000	50+
Problem Solvers	Patents Worldwide	Customers	Years

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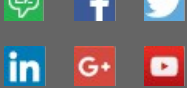
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