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LMK00101BEVAL

Universal Input

LMK00101BEVAL

Ultra-low Jitter LVC

Description & Features

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Key Document

LMK00101 User's Guide

(PDF 815 KB)

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Description

The LMK00101 Evaluation Board simplifies evaluation of the LMK00101 Ultra-low Jitter LVC

LMCMOS Fanout Buffer/Level Translator with Universal Input. Configuring and controlling the board is accomplished using on board switches.

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LMK00101BEVAL/NOPB: LMK00101 Ultra-low Jitter LVC	(USD) In Stock Typically Ships in 1 to 3 Business Days In Stock Typically Ships in 1 to 3 Business Days Buy from TI	Pricing may vary. Buy from distributor	ACTIVE
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