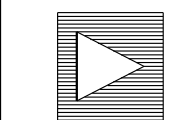
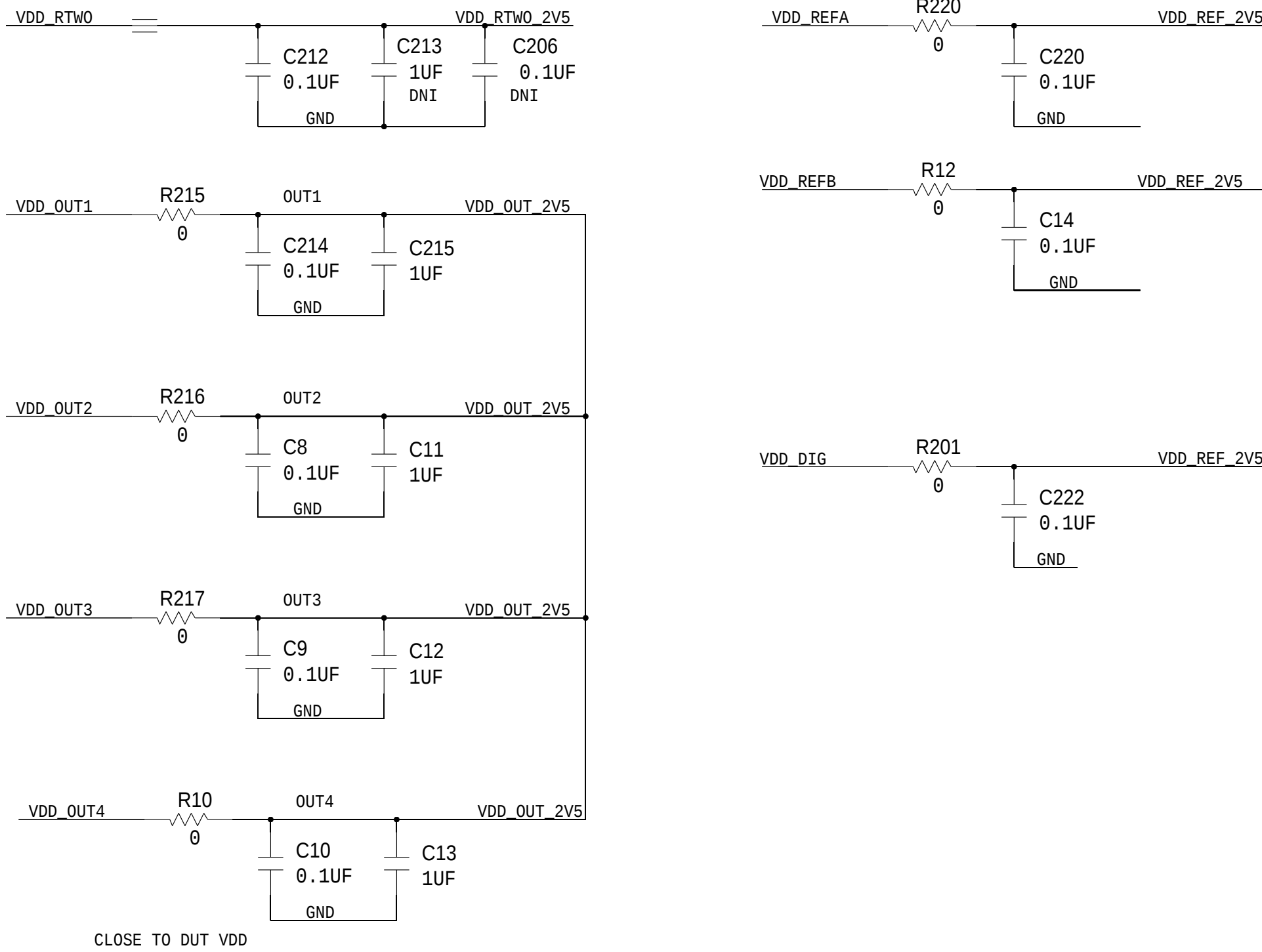
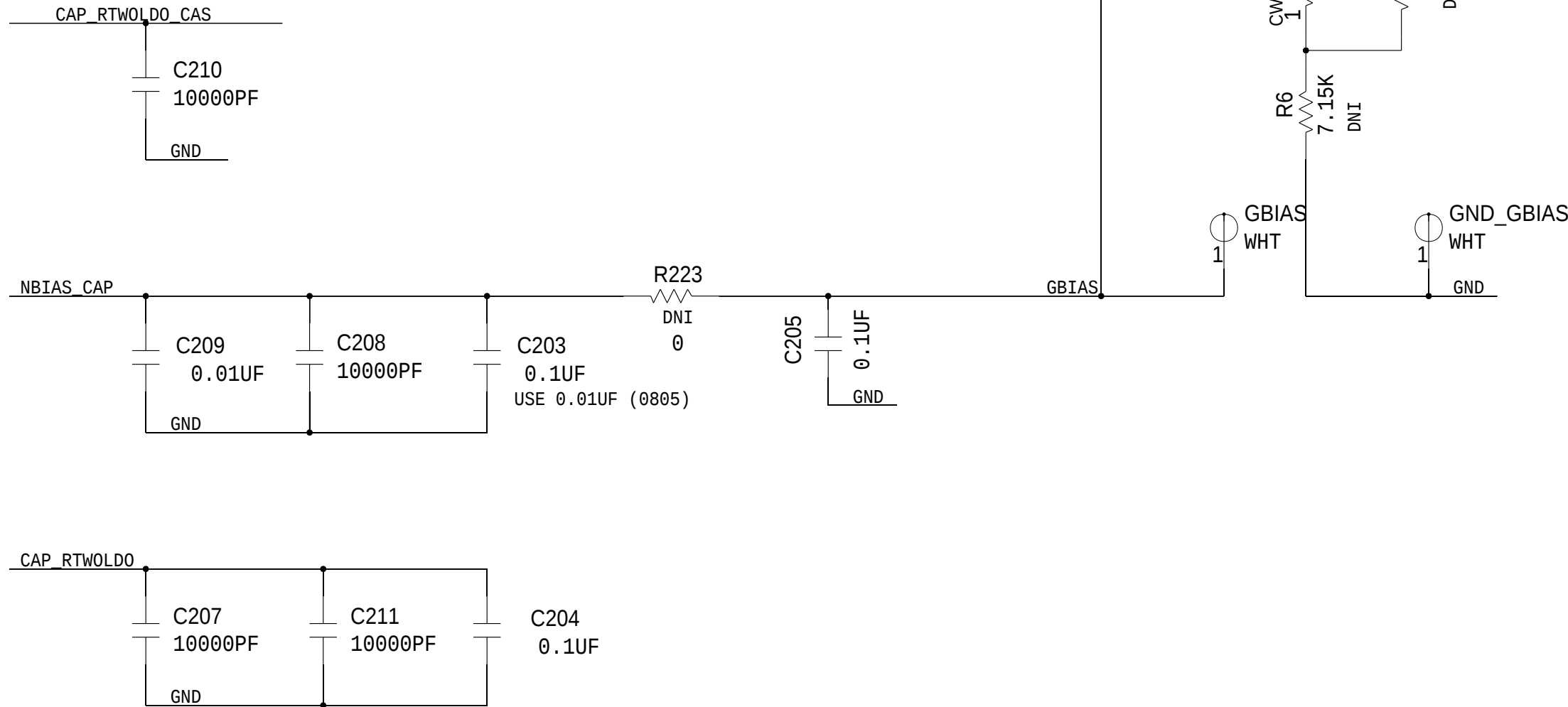
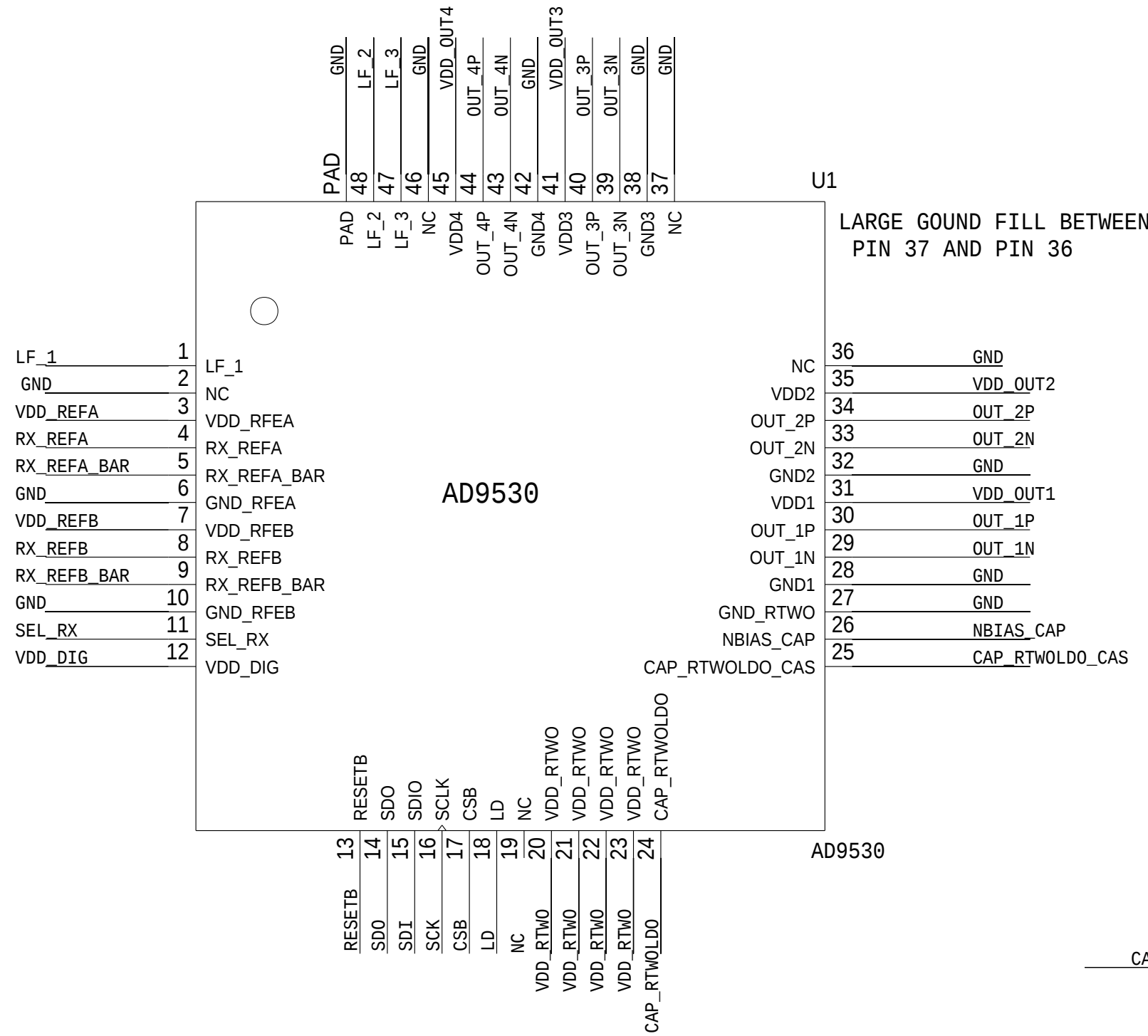
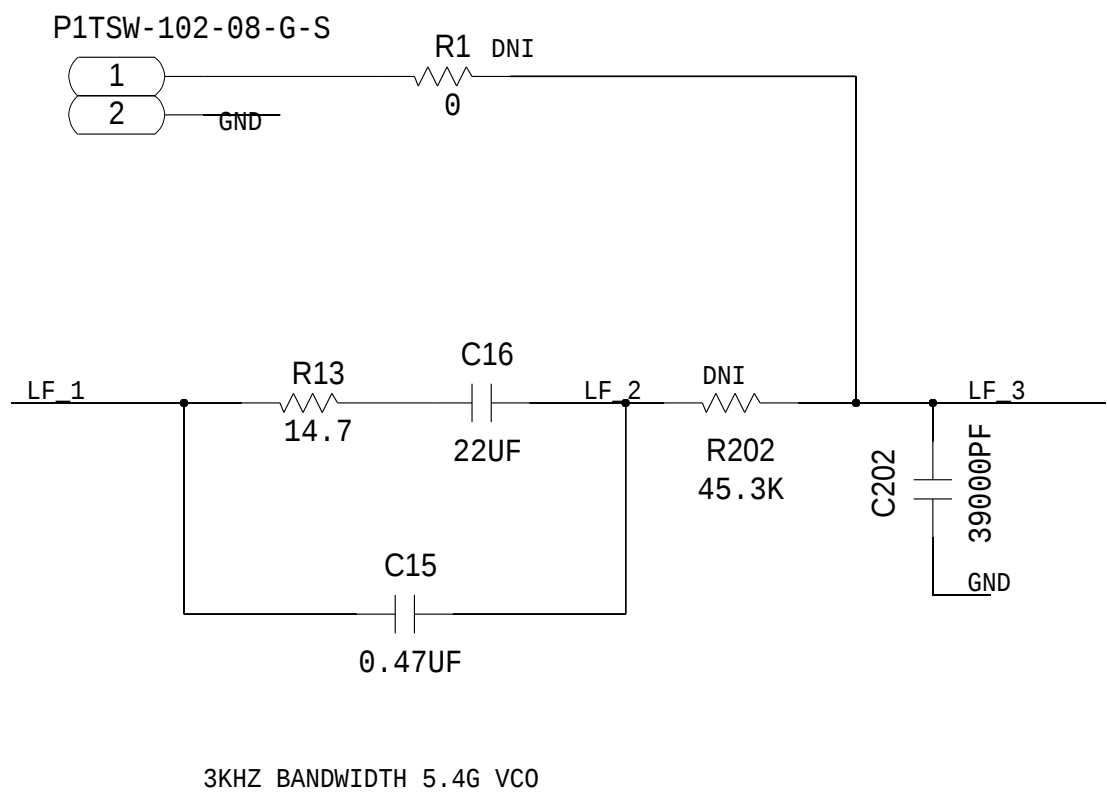


	8	7	6	5	4	3	2	1																														
D	THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.					JUMPER TABLE <table><tr><td>JP#</td><td>ON</td><td>OFF</td></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table>			JP#	ON	OFF	1			2			3			4			5			REVISIONS <table><tr><td>REV</td><td>DESCRIPTION</td><td>DATE</td><td>APPROVED</td></tr><tr><td>A</td><td>INITIAL RELEASE</td><td>04-07-14</td><td>ZHANGUO XI</td></tr></table>				REV	DESCRIPTION	DATE	APPROVED	A	INITIAL RELEASE	04-07-14	ZHANGUO XI
	JP#	ON	OFF																																			
	1																																					
	2																																					
	3																																					
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	5																																					
	REV	DESCRIPTION	DATE	APPROVED																																		
	A	INITIAL RELEASE	04-07-14	ZHANGUO XI																																		
RELAY CONTROL CHART																																						
CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR																																		
C																																						
B																																						
A						TEMPLATE ENGINEER - HARDWARE SERVICES - HARDWARE SYSTEMS - TEST ENGINEER - COMPONENT ENGINEER - TEST PROCESS - HARDWARE RELEASE - DESIGNER V.JAYME II PTD ENGINEER Zhanguo Xi CHECKER -			DATE 04-07-14 04-07-14	SCHEMATIC HW TYPE : Customer Evaluation Product(s): AD9530a : N/A PACKAGE : 48-lead 7X7_MM LFCSP-family : Pitch-pitch StyleVendor Style		ANALOG DEV ICES	DRAWING NO. 02_039970		REV. A																							
	P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS X.XX +0.010 X.XXX +-0.005 FRACTIONS +-1/32 ANGLES +-2			SIZE D SCALE 1:1 CODE ID NO. CodeID	SHEET 1	OF 7																											
	8	7	6	5	4	3	2	1																														

DUT AND POWER SUPPLY FILTERING

SUPPLY	RANGE	CURRENT
VDD_REFA	2.5V+/-5%	20 MA
VDD_REFB	2.5V+/-5%	20 MA
VDD_DIG	2.5V+/-5%	60 MA
VDD_RTWO	1.8, 2.5V+/-5%	2000 MA
VDD1	2.5V+/-5%	100 MA
VDD2	2.5V+/-5%	100 MA
VDD3	2.5V+/-5%	100 MA
VDD4	2.5V+/-5%	100 MA



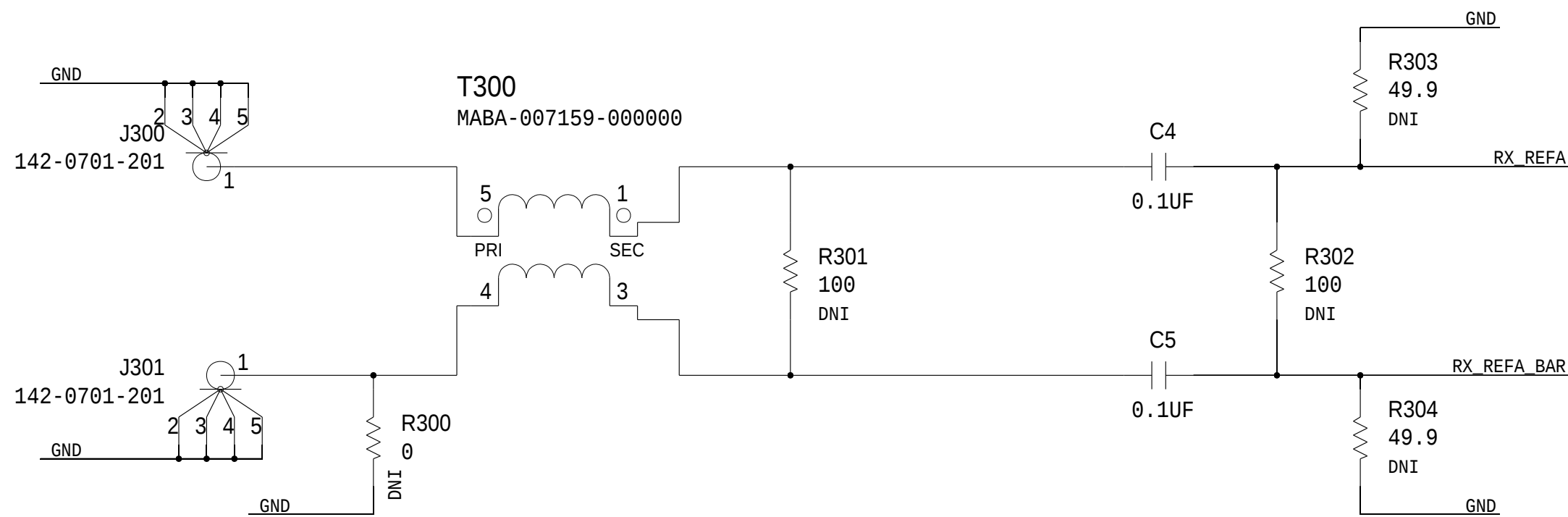
ANALOG
DEVICES

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OF ANALOG DEVICES.
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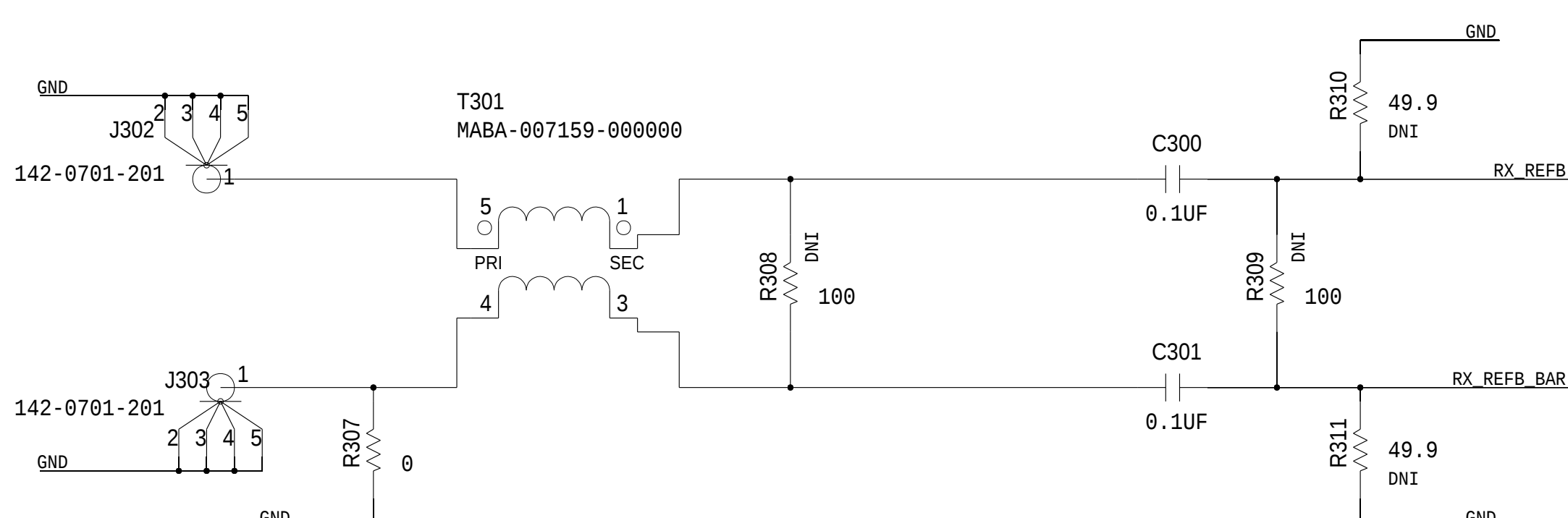
SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): AD9530a : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_039970	REV A	
PTD ENGINEER Zhanguo Xi	SIZE D	SCALE 1:1	SHEET 2 OF 7

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

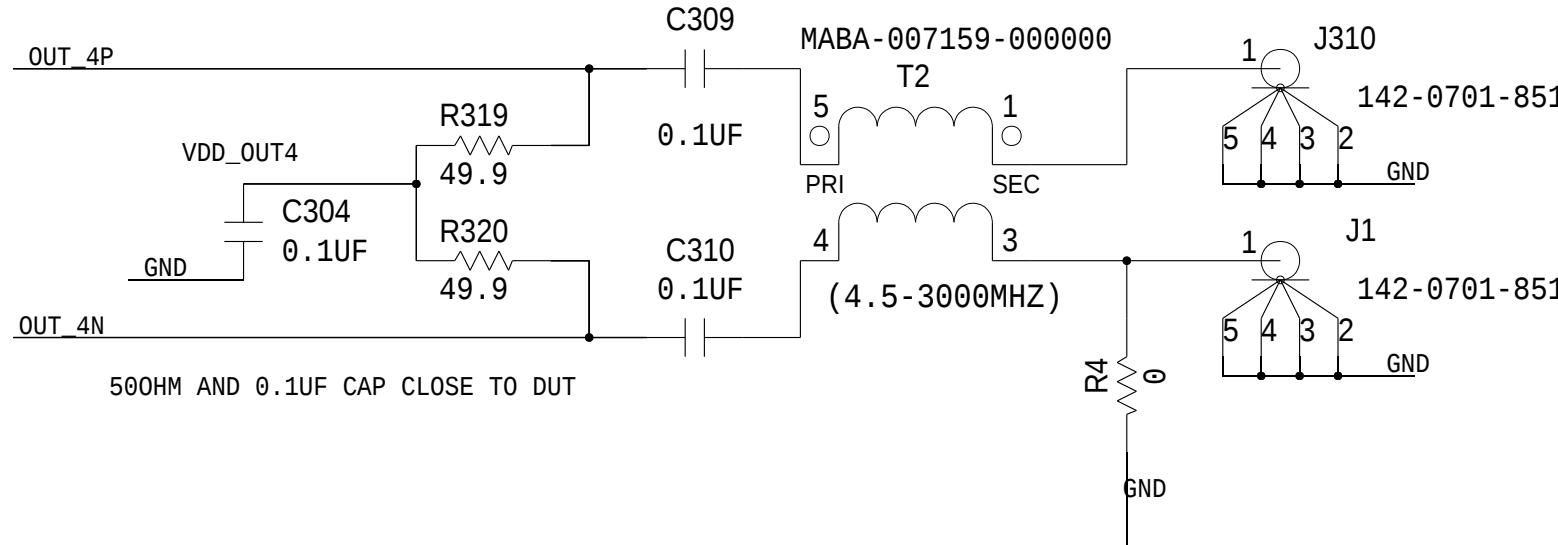
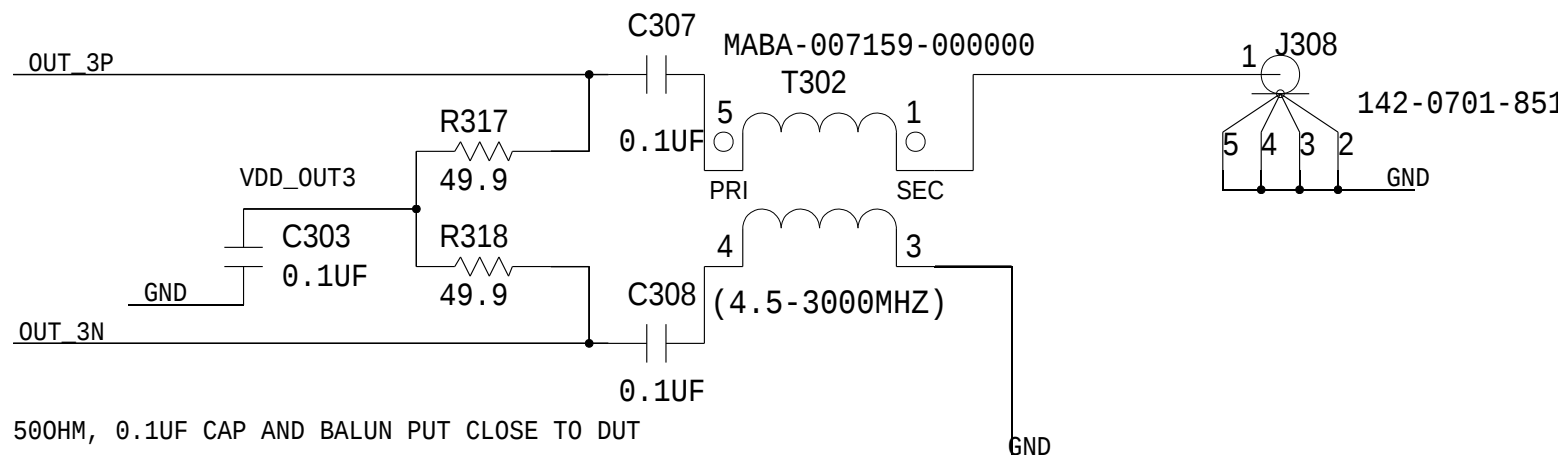
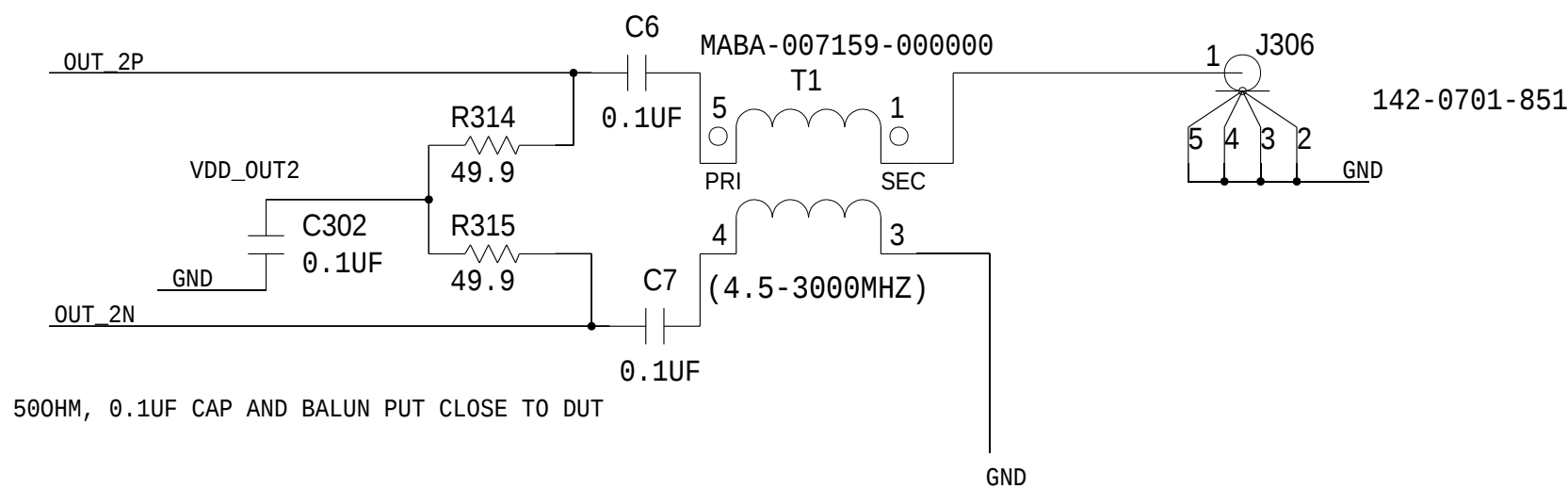
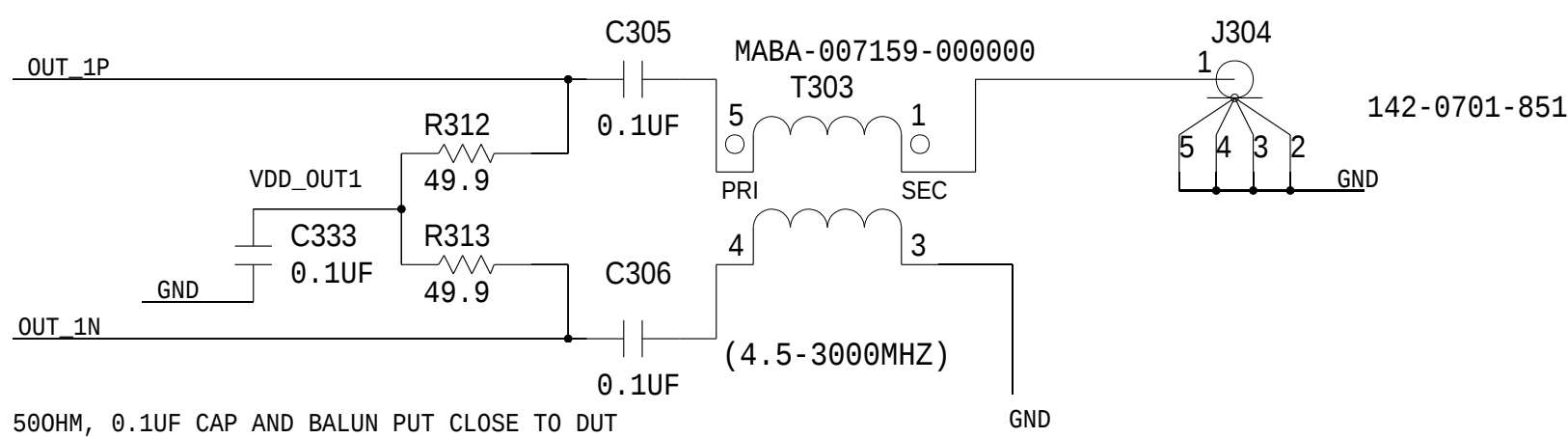
REFERENCEA



REFERENCEB



CLOCK OUTPUTS

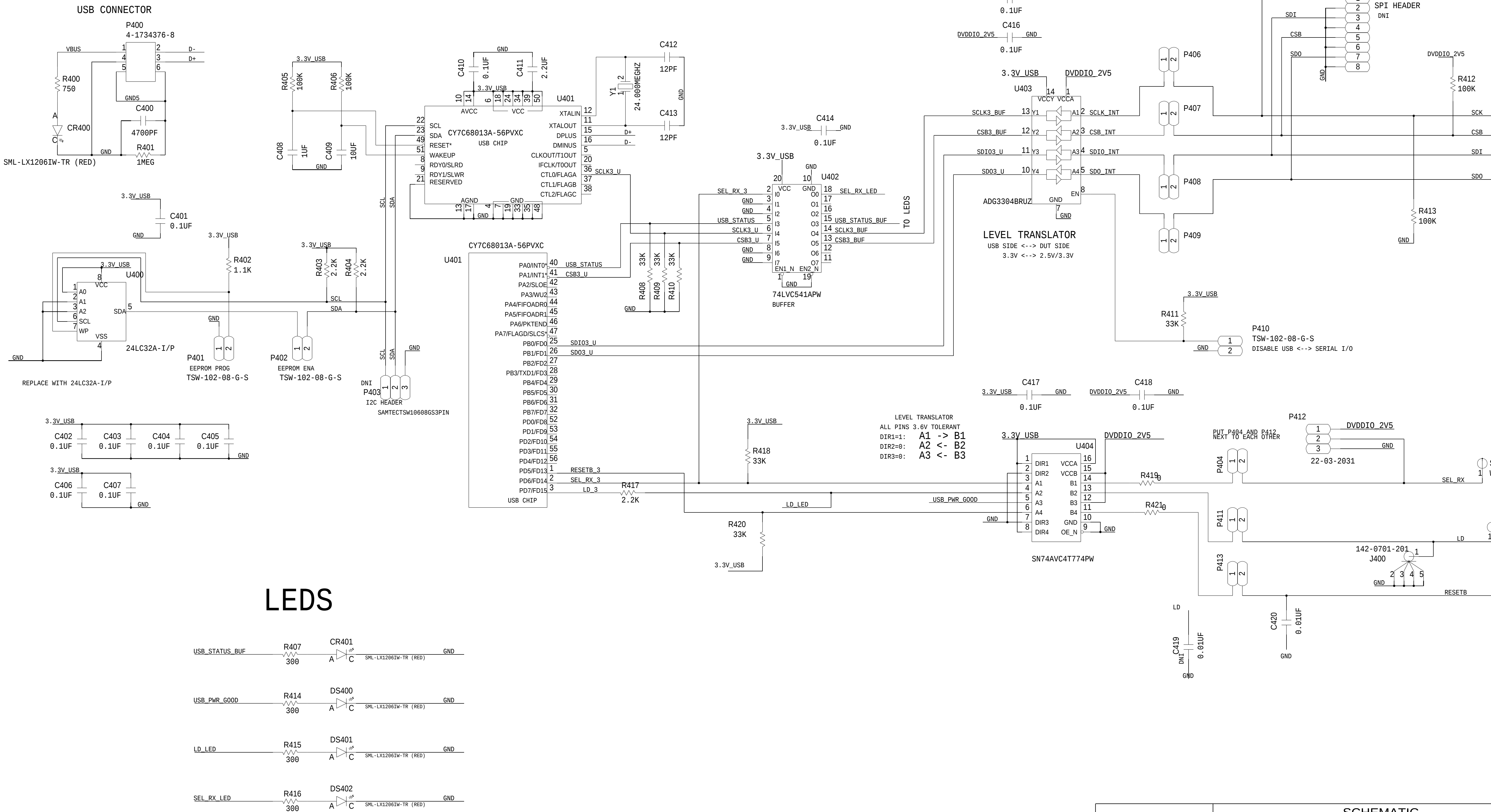


TRACES 50 OHMS SINGLE-ENDED, MATCHED LENGTH

SCHEMATIC			
HW TYPE : Customer Evaluation Product(s): AD9530a : N/A			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_039970	REV A	
PTD ENGINEER Zhanguo Xi	SIZE D	SCALE 1:1	SHEET 3 OF 7

USB SECTION

SERIAL/STATUS



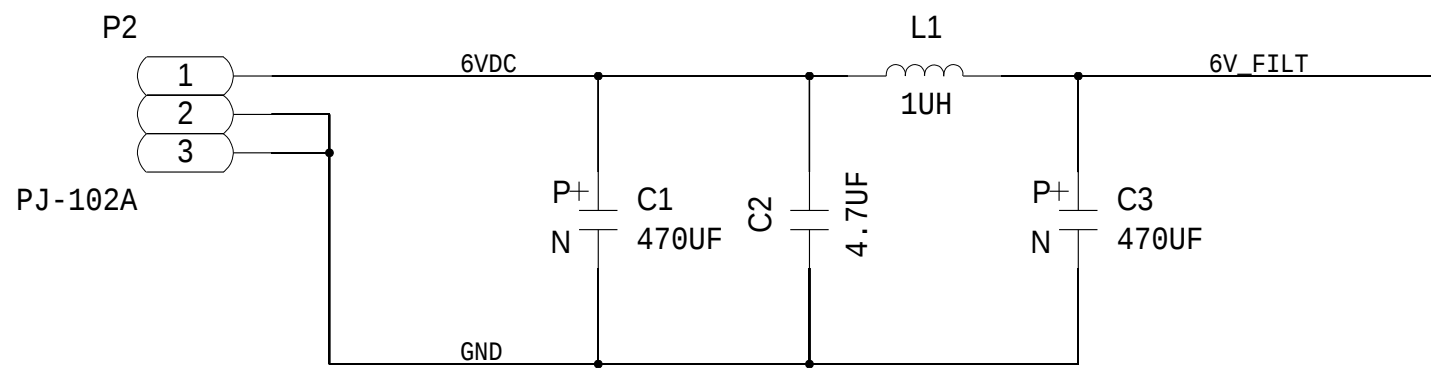
REVISIONS				
REV	DESCRIPTION	DATE	APPROVED	

SCHEMATIC				
HW TYPE : Customer Evaluation Product(s): AD9530a : N/A				
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_039970	REV A		
PTD ENGINEER Zhanguo Xi	SIZE D	SCALE 1:1	SHEET 4	OF 7

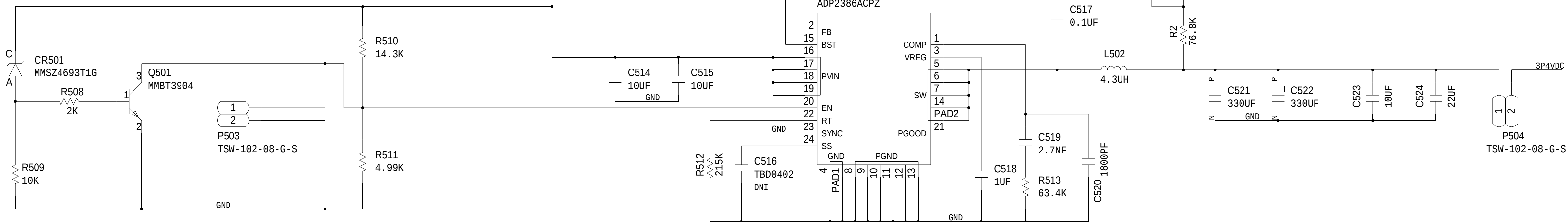
DC-DC CONVERTER

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

6V WALL CONNECTION

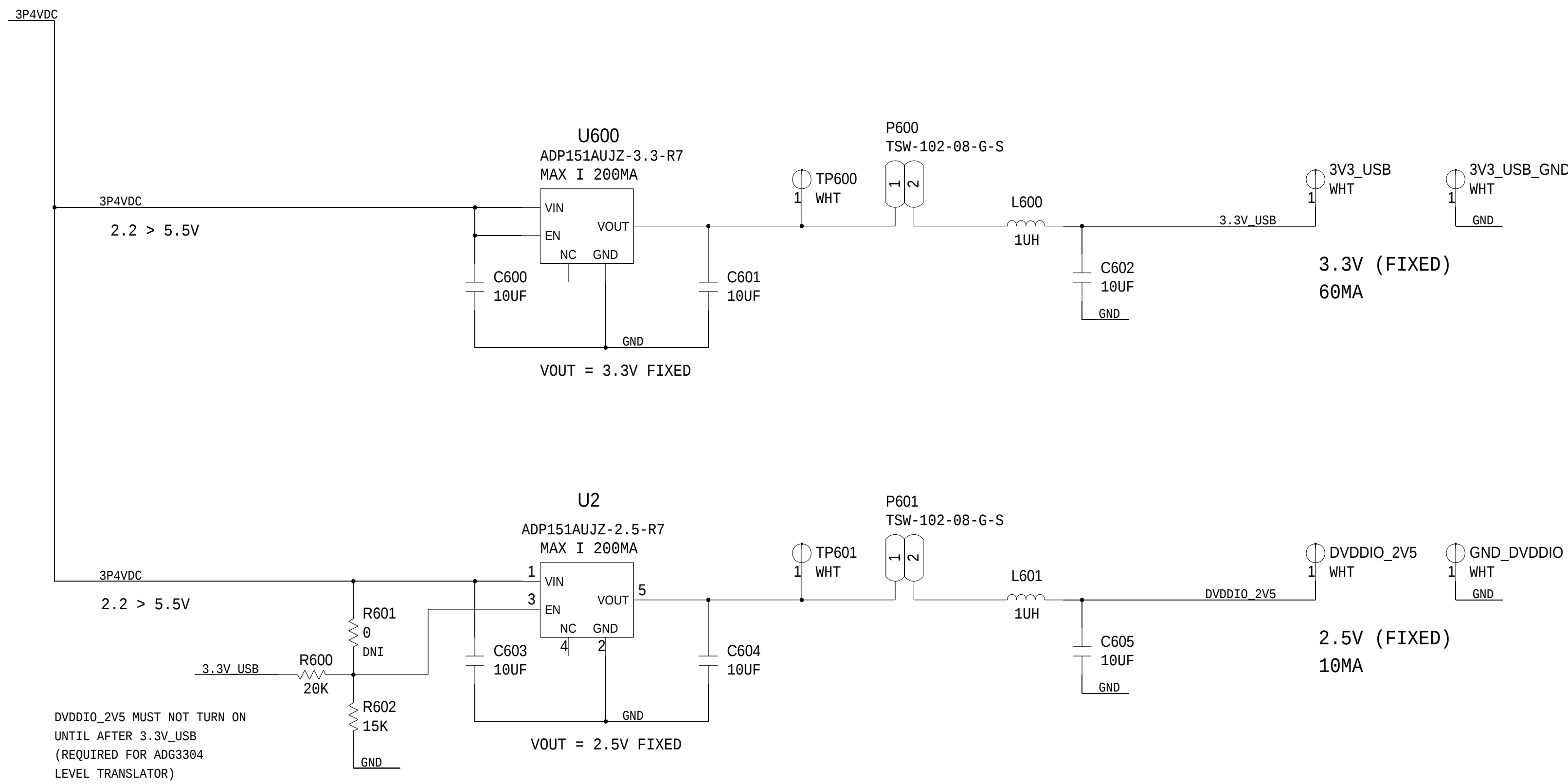


8.5V OVER PROTECTION CIRCUIT

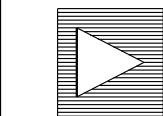


 ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s) : AD9530a N/A			
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	PTD ENGINEER Zhangyou Xi	SIZE D	SCALE 1:1	SHEET 5 OF 7

USB 2.5V & 3.3V REGS



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



ANALOG
DEVICES

SCHEMATIC

HW TYPE : Customer Evaluation
Product(s): AD9530a
: N/A

DESIGN VIEW
<DESIGN_VIEW>

PTD ENGINEER
Zhanguo Xi

DRAWING NO.
02_039970

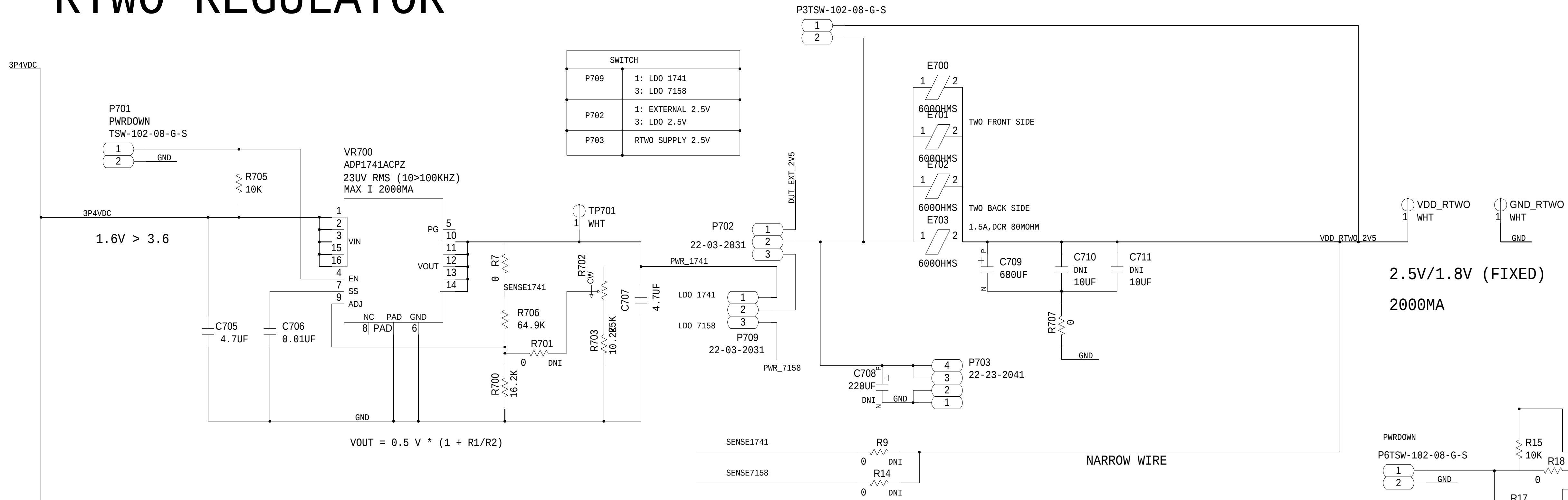
SIZE
D

SCALE
1:1

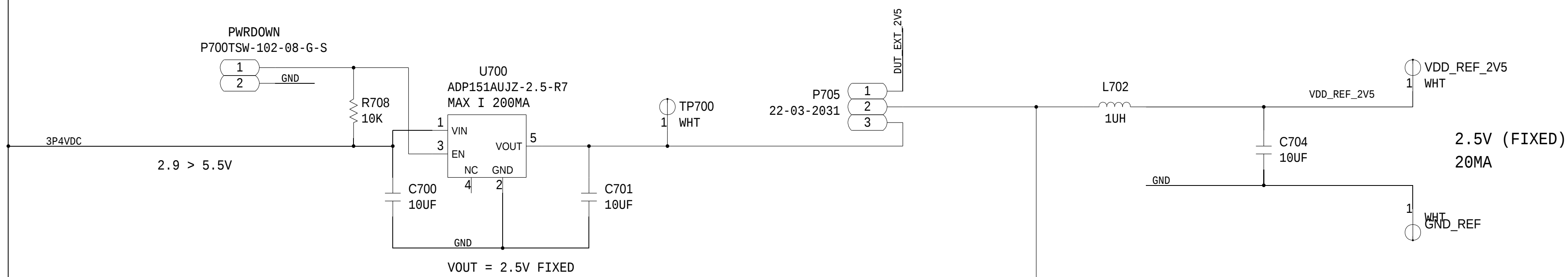
SHEET 6 OF 7

REV
A

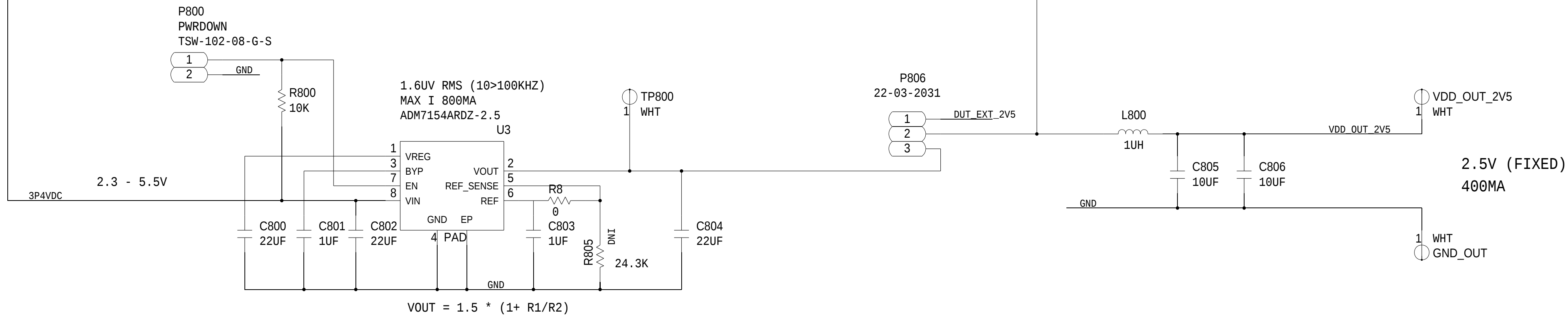
RTWO REGULATOR



REFA/B REGULATOR



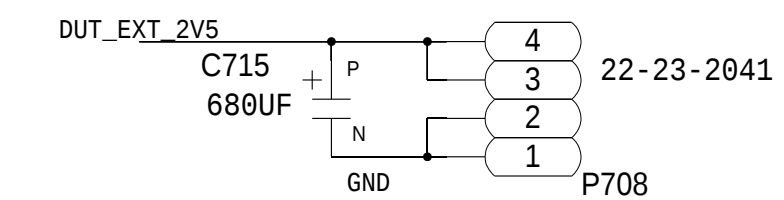
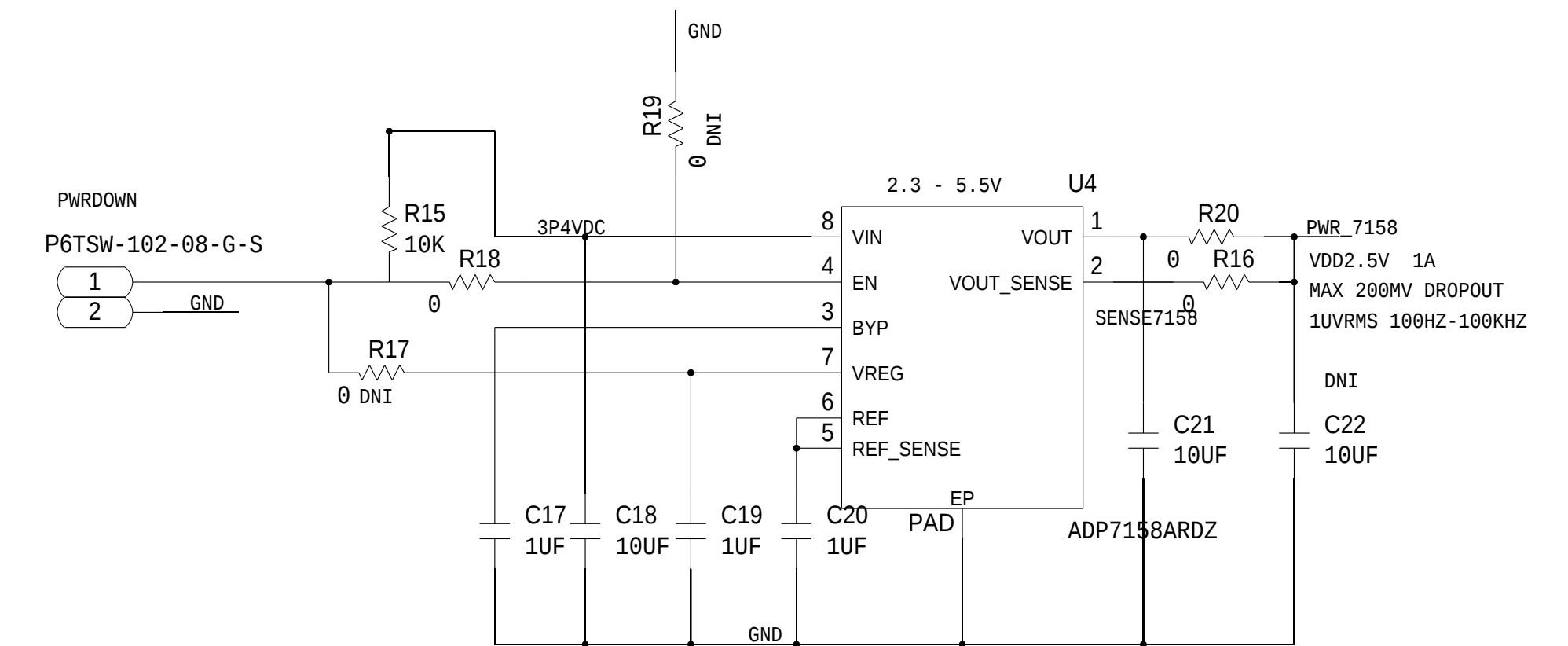
OUTPUT REGULATOR



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

VDD_RTW0_2V5

2.5V/1.8V (FIXED)
2000MA



 ANALOG DEVICES	SCHEMATIC			
	HW TYPE : Customer Evaluation Product(s) : AD9530a : N/A			
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	PTD ENGINEER Zhanguo Xi	SIZE D	SCALE 1:1	SHEET 7 OF 7